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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Akira YAMANOUE et al.**

Group Art Unit: **2823**

Serial No.: **10/673,354**

Examiner: **Toledo, Fernando L.**

Filed: **September 30, 2003**

Confirmation No.: **4037**

For: **METHOD FOR FABRICATING SEMICONDUCTOR DEVICE HAVING
GATE ELECTRODE TOGETHER WITH RESISTANCE ELEMENT (as
amended).**

Attorney Docket No.: **031736**

Customer Number: **38834**

AMENDMENT UNDER 37 C.F.R. § 1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

August 13, 2004

Sir:

In response to the Office Action dated May 14, 2004, Applicants amend the application as follows and submits the following remarks.

Amendments to the Title begin on page 2 of this paper.

Amendments to the Specification begin on page 3 of this paper.

Amendments to the Claims are reflected in the listing of claims that begins on page 4 of this paper.

Remarks/Arguments begin on page 9 of this paper.

Response under 37 C.F.R. §1.111

Attorney Docket No. **031736**

Serial No. **10/673,354**

Amendment to the Title:

Please replace the Title of the Application with the following amended Title:

METHOD FOR FABRICATING SEMICONDUCTOR DEVICE HAVING GATE
ELECTRODE TOGETHER WITH RESISTANCE ELEMENT.